

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Rupal Parikh

Art Unit:

2611

Serial No.:

10/038,956

9999999999 Examiner:

Linda Wong

Filed:

January 2, 2002

Docket:

ITL.0654US

P12395

For:

Synchronizing Data or

Signal Transfer Across

Assignee:

Intel Corporation

Clocked Logic Domains

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION

Sir:

In response to the office action mailed May 3, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: July 12, 2006

I hereby Certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the